

ABSTRACT OF THE DISCLOSURE

In a clock squarer having a semiconductor chip pad and a square wave generating circuit, the clock squarer can generate a square wave having stable duty, irrespective of variation in temperature, process or supply voltage. In the

5 clock squarer, a capacitor is provided between the chip pad and the square wave generating circuit. The clock squarer in accordance with the present invention is especially applicable to processes that involve high leakage current or a fabrication process for products requiring high-speed operation.

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